

Nonlinear Optimized Fast Locking PLL; using Genetic Algorithm

M. Zabih* and H. Miari-Naimi*

Abstract: This paper presents a novel approach to obtain fast locking PLL by embedding a nonlinear element in the loop of PLL. The nonlinear element has a general parametric Taylor expansion. Using genetic algorithm (GA) we try to optimize the nonlinear element parameters. Embedding optimized nonlinear element in the loop shows enhancements in speed and stability of PLL. To evaluate the performance of the proposed structure, various tests performed and results compared with standard phase locked loop. The tests and results show the superior performance of the proposed PLL.

Keywords: PLL, Genetic Algorithm, Nonlinear Element, Fast lock.

1 Introduction

Phase locked loops have wide applications in communication circuits such as radio frequency transmitters, wireless communications, optical receivers and etc. They also serve different systems as a main building block. As example, systems like frequency modulators, frequency synthesizers, clock and data recovery circuits can be addressed [1-10]. The performance of PLL is critical for the above applications so that high performance PLLs was the subject of many researches in the field of electronics and communication system design. The most important performance measure of PLLs is the capture range and the speed of locking. Fast locking PLLs provide the mentioned systems to work in higher speeds. There are some works on speeding up the lock process. In [8] using an adaptive system, the bandwidth of loop filter is controlled based on the lock condition. When the loop is far from locked condition a large value for bandwidth is chosen that causes higher loop gain and consequently smaller rise time. To avoid large overshoots the bandwidth is reduced when the loop closes to locked condition. It is clear that this method requires an efficient subsystem to measure the distance to lock condition. The paper mentioned nothing about the detail of the subsystem. In another approach a nonlinear element is embedded to the loop [9]. Nonlinear element has a profile implementing somehow the idea of [10]. To perform this, the slope of transfer function is chosen inconstant and varies for different values of its input.

Some significant questions arise here such as: what is the best or optimal nonlinear element for the loop? Is there any straightforward way to find optimal nonlinear element analytically or numerically. This paper introduces a novel method that one can find the best nonlinear element using genetic algorithm. Indeed the algorithm searches in mathematic functions space. The rest of paper is as follows: in section 2 the dynamic of a type I PLL is introduced and the tradeoff between overshoot and rise time is addressed. Section 3 deals with Genetic Algorithm method for optimization of the nonlinear element parameters. This method has a major role in proposed PLL. Section 4 introduces the new nonlinear PLL and related differential equations and also the related genetic algorithm finding optimal solutions. In this section, the nonlinear element is optimized using Genetic Algorithm. Finally the conclusion is given in section 5.

2 Dynamic of Phase Locked Loop; A Brief Review

A phase locked loop is a system which generate a periodical output in phase to an arbitrary periodical input. A conceptual or mathematical phase locked loop has a general block diagram as shown in Fig. 1. This block diagram represents the dynamics governing on the PLL.

In Fig. 1 the first block is phase detector, PD, that measures the phase difference between input and output waveforms. The second block is a voltage controlled oscillator, VCO that generates a periodical output with a frequency depending to the phase difference. The philosophy of above block diagrams can be found in text books [1-2]. In Fig. 1, ψ_{in} and ψ_{out} are the input and output phases defined as $\psi_{in}(t) = \omega_i t$ and based on

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* The Authors are with the Department of Electrical and Computer Engineering, Babol University of Technology, Babol, Iran.
E-mails: mariam.zabih@gmail.com, h_miare@nit.ac.ir.

the definition of VCO: $\psi_{out}(t) = \omega_{out}t = \omega_c t + \int k_{vco} v_1 dt$. The signal v_1 is the control voltage of VCO and is proportional to phase difference by a coefficient of K_{PD} , and ω_c is the central frequency of VCO. To find the dynamic of the above system, we can write:

$$\psi_i - \psi_o = \psi_e = \omega_i t - \omega_c t - \int k_{vco} v_1 dt \quad (1)$$

$$\dot{\psi}_e = \omega_i - \omega_c - k_{vco} v_1, v_1 = k_{PD}(\psi_i - \psi_o) = k_{PD}\psi_e \quad (2)$$

$$\dot{\psi}_e = \omega_i - \omega_c - k_{vco} k_{PD} \psi_e \quad (3)$$

Equation (1) is a first order differential equation describing the behavior of the system. As a direct consequence of Eq. (1), when the system reaches to a steady state (locked condition) we have $\dot{\psi}_e = 0$

resulting in $\omega_{out} = \omega_i$ and $\psi_e = \frac{\omega_i - \omega_c}{k_{vco} k_{PD}}$. The equation

above can be simply rewritten in term of control voltage. The step response of the system above has an exponential form with time constant of $\frac{1}{k_{vco} k_{PD}}$, larger

loop gain, higher speed in locking process. The above differential equation was written in term of total phase of input an output, without loss of generality above equations can be represented in term of excess phase defined as $\psi_{ex,in} = \psi_i - \omega_c t$, $\psi_{ex,out} = \psi_o - \omega_c t$ without any changes in equations.

In practical type-I PLLs phase detectors usually require a low pass filter as shown in Fig. 2 [1-2]. So to have a more general and practical model, we consider the dynamical model of Fig. 2 for a PLL.

The equations governing on the model above can be written as follows (ψ_{in}, ψ_{out} are excess phases).

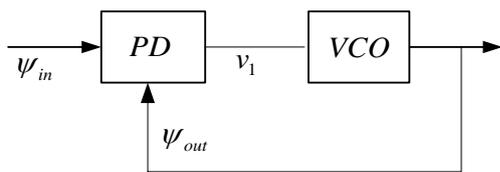


Fig. 1 Block diagram of a conceptual PLL

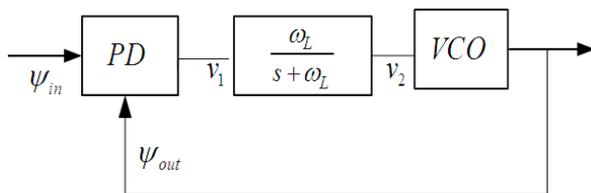


Fig. 2 Dynamical model of a second order type-I PLL

$$v_1 = k_{PD}(\psi_{in} - \psi_{out}), \psi_{out} = \int k_{vco} v_2 dt \quad (4)$$

$$\frac{v_2(s)}{v_1(s)} = \frac{\omega_L}{s + \omega_L} \Rightarrow \dot{v}_2(t) + \omega_L v_2(t) = \omega_L v_1(t) \quad (5)$$

$$\begin{aligned} \psi_e = \psi_{in} - \psi_{out} &\Rightarrow \dot{\psi}_e = \dot{\psi}_{in} - \dot{\psi}_{out} \\ \dot{\psi}_e = (\omega_i - \omega_c) - (\omega_c + k_{vco} v_2 - \omega_c) & \\ = \omega_i - \omega_c - k_{vco} v_2 & \end{aligned} \quad (6)$$

$$v_1 = k_{PD} \psi_e \Rightarrow \frac{\dot{v}_1}{k_{PD}} = \dot{\psi}_e = \omega_i - \omega_c - k_{vco} v_2 \quad (7)$$

From Eqs. (7) and (5)

$$\frac{\ddot{v}_2}{k_{PD} \omega_L} + \frac{\dot{v}_2}{k_{PD}} = \omega_i - \omega_c - k_{vco} v_2 \quad (8)$$

$$\ddot{v}_2 + \omega_L \dot{v}_2 + k_{PD} \omega_L k_{vco} v_2 = k_{PD} \omega_L (\omega_i - \omega_c) \quad (9)$$

Equation (9) is the dynamical behavior of second order PLL.

This dynamic in term of input excess phase and output excess phase can be described by closed loop transfer function of Eq. (10).

$$H(s) = \frac{\omega_n^2}{s^2 + 2\xi\omega_n s + \omega_n^2} \quad (10)$$

where ξ and ω_n are as Eq. (11).

$$\omega_n = \sqrt{K_{VCO} K_{PD} \omega_{LPF}}, \xi = \frac{1}{2} \sqrt{\frac{\omega_{LPF}}{K_{VCO} K_{PD}}} \quad (11)$$

The transfer function of Eq. (10) has two poles as shown in Eq. (12). The root locus of Eq. (10) is shown in Fig. 3. For $\xi > 1$ both poles are real. By decreasing ξ equivalently increasing of loop gain the poles becomes complex.

$$s_{1,2} = \omega_n (\xi \pm \sqrt{\xi^2 - 1}) \quad (12)$$

The Fig. 4 shows the step response of the system for different value of ξ .

As Fig. 4 shows, there is a tradeoff between rise time and overshoot where small rise time results in big overshoot. So controlling ξ , one can not reach to a very fast locking system.

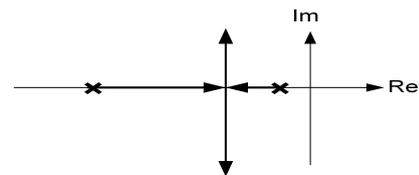


Fig. 3 The root locus of transfer function of Eq. (10)

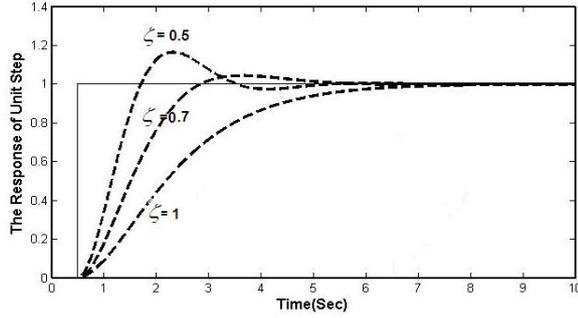


Fig. 4 the step response of system for different values of ζ

Settling time, rise time and overshoot of the system above are derived as Eqs. (13) to (15) from [1, 2].

$$t_s = \frac{4.8}{\zeta\omega_n}, \quad \zeta\omega_n = \frac{1}{2}\omega_L \quad (13)$$

$$t_r = \frac{\pi - \cos^{-1}\zeta}{\omega_n\sqrt{1-\zeta^2}} \quad (14)$$

$$M_p = e^{\frac{-\pi\zeta}{\sqrt{1-\zeta^2}}} \quad (15)$$

As mentioned above there are a tradeoff that prevents ideal step response of system. To break down this tradeoff we embed a parametrical nonlinear element in the loop and try to tune the parameters for best step response.

3 Genetic Algorithm; A Brief Review

Genetic Algorithms (GAs) are adaptive heuristic search algorithm based on the evolutionary ideas of natural selection and genetics. The basic hypothesis of this algorithm is that the congenital patterns in each generation transmit with gens to next generation. The initial parameters are selected randomly and form the initial population [11-15]. The population evolves from The Genetic Algorithm process is explained as follows:

1. Initial parameters definitions: Such as number of population, number of generation, mutation probability, crossover probability and chromosome length.

2. Producing Initial population: Initial population is generated randomly from search space.

3. Evaluation of each chromosome: each chromosome of population is assigned a value as fitness, based on objective function.

4. Testing stop condition: If the end condition such as number of generation is satisfied, the algorithm stops and returns the best solution in current population.

5. Producing new population: In this state a new population is created by repeating following steps until the new population is complete. According to the chromosome evaluation, two chromosomes are selected from the population as parents and genetic operators are applied on them. Crossover mixes two parent

chromosomes and creates new offspring with probability of P_c . Mutation operator changes random bit from 1 to 0 or vice versa with a small probability of mutation (P_m) which is much smaller than P_c .

6. Replacing populations: in this state, current population is replaced with the new population.

7. Loop: Go to step 2 and repeat [11, 13].

4 Proposed Nonlinear PLL

4.1 System Overview

In proposed phase locked loop a nonlinear element is embedded into the loop of PLL, hopping obtain faster transient. Shown in Fig. 5 is the proposed PLL structure, which is almost the same as [9]. In this method we fined the optimal nonlinear element aiming best speed. To obtain the equations governing the system, considering Fig. 5, we can write Eqs. (16)-(22).

Equation (21) is the second order nonlinear equation describing the system. To evaluate the locking speed in locked conditions the related differential equation is as Eq. (22).

We evaluate the response for a step in input phase and hope to have similar step in output phase in an ideal condition. For generality, the nonlinear element can be

$$v_1 = k_{PD}(\psi_{in} - \psi_{out}) \quad (16)$$

$$\dot{v}_2 + \omega_L v_2 = \omega_L v_1 \quad (17)$$

$$\dot{\psi}_{out} = k_{vco} v_3 = k_{vco} f(v_2) \quad (18)$$

$$\dot{v}_1 = k_{PD}(\dot{\psi}_{in} - \dot{\psi}_{out}) = k_{PD}(\omega_i - \omega_c - k_{vco} f(v_2)) \quad (19)$$

$$\frac{\ddot{v}_2}{\omega_L} + \dot{v}_2 = k_{PD}(\omega_i - \omega_c - k_{vco} f(v_2)) \quad (20)$$

$$\ddot{v}_2 + \omega_L \dot{v}_2 + k_{PD}\omega_L k_{vco} f(v_2) = k_{PD}\omega_L(\omega_i - \omega_c) \quad (21)$$

$$\ddot{v}_2 + \omega_L \dot{v}_2 + k_{PD}\omega_L k_{vco} f(v_2) = 0 \quad (22)$$

considered as $f(v) = \sum_{n=0}^N a_n v^n$, having capability of

approximating every desired nonlinear function. For finding appropriate coefficients of $f(v)$, at first N , the order of polynomial should be specified. In fact N is number of gens in this optimization problem. Therefore we have a optimization problem with N parameters which can be solve by Genetic Algorithm. This process is shown in Fig. 6.

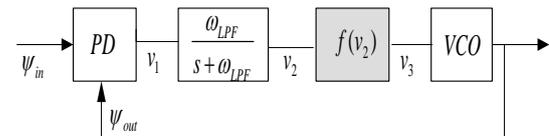


Fig. 5 The nonlinear PLL

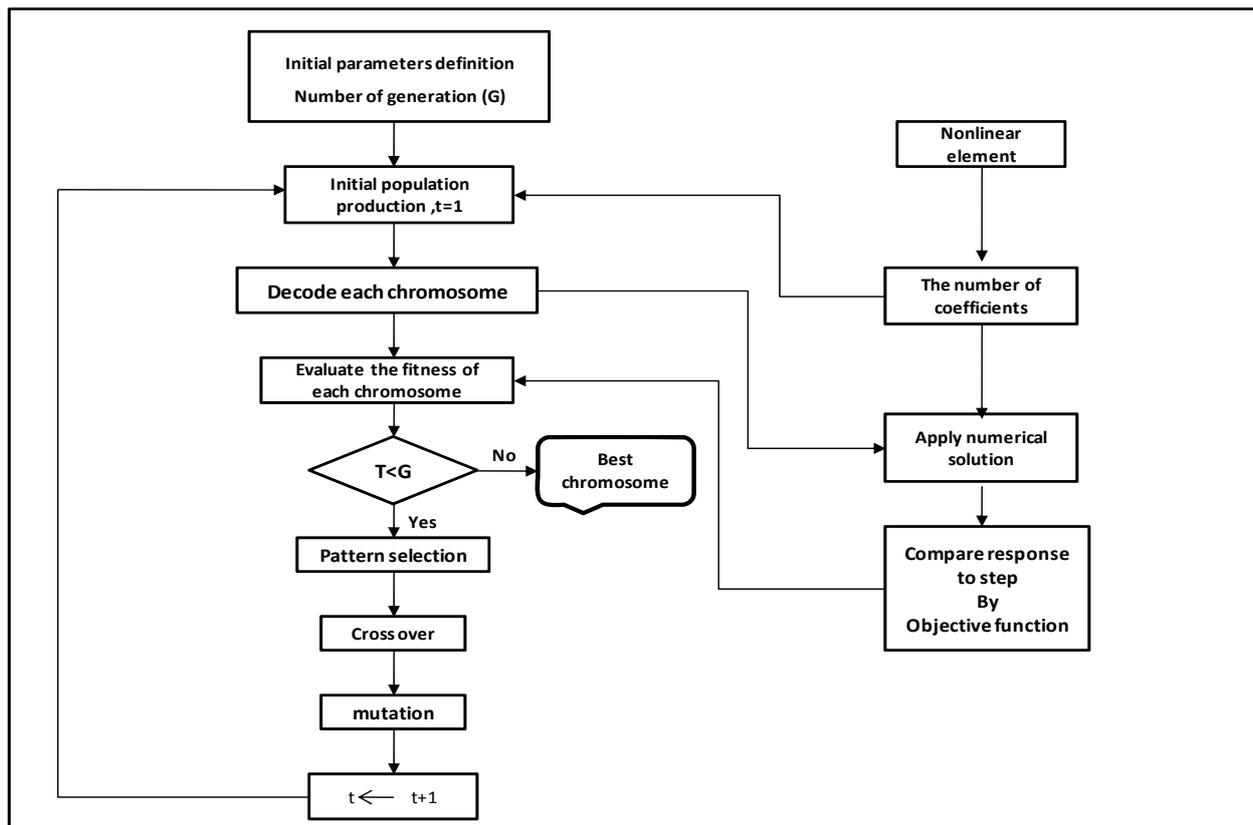


Fig. 6 Flowchart of proposed genetic approach

4.2 Optimization process

To optimize the nonlinear element we suppose that the input frequency of PLL is the same as central frequency of VCO, so the control voltage of VCO in steady state is zero. Under condition mentioned a phase step is applied on the input. The related differential equation is as (46).

$$\ddot{v}_2 + \omega_L \dot{v}_2 + k_{PD} \omega_L k_{vco} f(v_2) = 0 \quad (23)$$

For fair comparison, three PLL are considered. The standard PLLs have a same 3dB frequency which is equal to 2 Hz in this case. The first is a linear PLL with $\omega_n = 1, \zeta = 1$ that naturally has a slow response. The next is a well tuned PLL with $\omega_n = 0.5, \zeta = 0.707$. These two are used to evaluate how the nonlinear PLL which is named by NPLL, works better than traditional linear ones.

It is convenient to choose $\xi > 1$ mainly because of mismatches and deviations caused by temperature and process [1,2]. As mentioned above the nonlinear PLL is compared by well tuned linear PLLs for a worse case comparison. A unity step is applied to input phase and the output profile is evaluated.

In this case, the initial parameters are selected as follows:

- The number of population: 100
- The number of generation: 25
- Mutation probability: 0.1

- Crossover probability: 0.9
- Chromosome length: according to desired accuracy is predefined as N , the number of coefficient. In this case, the accuracy of first coefficient is two decimal places and for other coefficients is one decimal places.

The coefficients of each initial population are generated randomly and expressed in a binary format.

$$PLL_1 : \omega_n = 1, \zeta = 1 \Rightarrow \ddot{v} + 2\dot{v} + v = 0$$

$$PLL_2 : \omega_n = 0.5, \zeta = 0.707 \Rightarrow \ddot{v} + 2\dot{v} + v = 0$$

(24)

$$NPLL : \ddot{v} + 2\dot{v} + \sum_{n=1}^N a_{2n-1} v^{2n-1} = 0$$

Concatenating the binary formatted of parameters a chromosome is created. The fitness of each chromosome in the population is evaluated by the related value of objective function. The optimal response to a step in the input phase is step function in output phase. Therefore to evaluate a chromosome somehow we measure the distance of corresponding response from ideal step. So objective function is defined by Eq. (25):

$$\int_0^T |\Delta\phi - K_{vco} \int_0^t f(v(\tau)) d\tau| dt \quad (25)$$

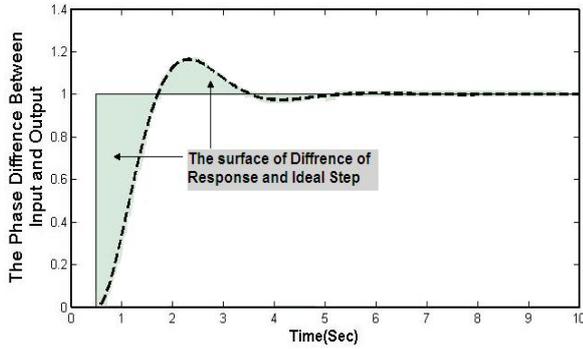


Fig. 7 The sum of surfaces between ideal step and actual response

where T is the time that system needs to be set on steady state. Equation (25) is indeed the surface between desired step and actual response that should be minimized as shown in Fig. 7.

5 Simulation and Result

The nonlinear element is considered as $f(v) = \sum_{n=0}^N a_{2n-1} v^{2n-1}$. It's tried to find the best and optimal value of the coefficients. For first test, N is considered 4. It means that the number of coefficient that must be optimized is four and so $f(v)$ could be rewritten as:

$$f(v) = a_1 v + a_3 v^3 + a_5 v^5 + a_7 v^7 \quad (26)$$

Fig. 8 shows the control voltage of VCO for PLL_1 , PLL_2 and $NPLL$ optimized by GA. The control voltage of VCO is derivative of output phase, so desiring response close to step means that in ideal case the output phase has a step profile and equivalently the control voltage has an impulse shape. The shape close to impulse is describe with two factor; first having thin width and high amplitude and second not cross zero. As it's seen the control voltage of PLL_1 and PLL_2 are wide and have less similarity to impulse. However, the control voltage related to $NPLL$ is more thinner with higher amplitude and also it doesn't cross the zero. Therefore it is more similar to impulse function in respect of others.

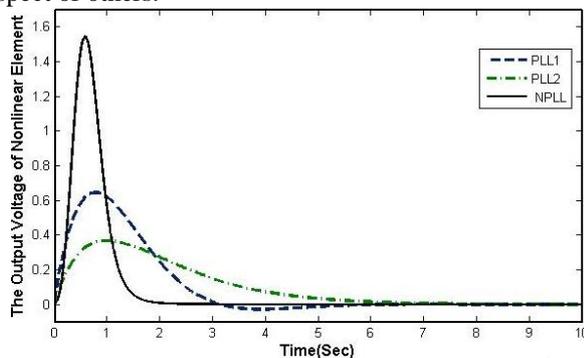


Fig. 8 The control voltage of VCO of PLL_1 , PLL_2 and $NPLL$

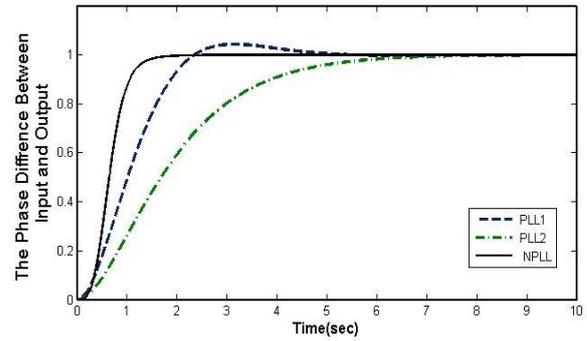


Fig. 9 The Phase step response of PLL_1 , PLL_2 and $NPLL$

In Fig. 9 the step response of output phase of $NPLL$ is closer to ideal step in comparison with PLL_1 and PLL_2 considerably; because of lesser settling time and rise time. Also, The $NPLL$ hasn't any considerable overshoot.

Table 1 shows the overshoot, rise time and settling time of the nonlinear PLL in comparison to two linear one. As it is shown the $NPLL$ has better step response feature features rather than the two others.

Obviously if N has greater value, the approximation of $f(v)$ is more precise. The performance of $NPLL$ for different value of N is shown by Table 2.

Fig. 10 shows that the step response for $NPLL$ with the different number of coefficients. The phase step response is more close to ideal step while $N=5$ in comparison with other value of N which is confirmed by the result of the transient response. It must be mentioned that the overshoot is equal to zero for all $NPLL$ s. In return for the accuracy of approximation of $f(v)$, the running time of optimization program increases. This means that there is a trade off between the accuracy of approximation of $f(v)$ and optimization time.

Table 1 The performance measure of three PLLs

	Overshoot (sec)	Rise time (sec)	Settling time (sec)
PLL_1	0	3.46	4.8
PLL_2	4.32%	2.35	4.8
$NPLL$	0	0.71	1.22

Table 2 The performance measure of three $NPLL$ s

	Running time (sec)	Rise time (sec)	Settling time (sec)
$N = 3$	315.67	0.76	1.31
$N = 4$	347.10	0.71	1.22
$N = 5$	367.29	0.64	1.10

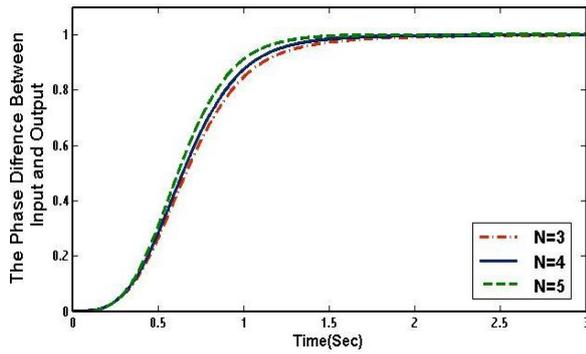


Fig. 10 The Phase step response of NPLL for different number of coefficients N

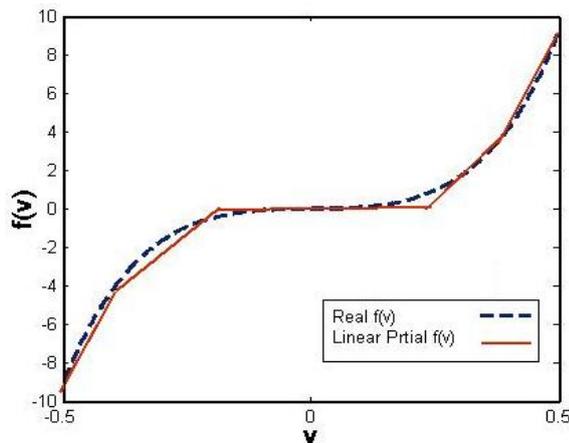


Fig. 11 The graph of $f(v)$ and linear partial function for $N=4$

Table 3 The value of coefficients of $f(v)$

	a_1	a_3	a_5	a_7	a_9
$N = 3$	0.33	45.0	94.1	-	-
$N = 4$	0.17	54.4	54.7	78.0	-
$N = 5$	0.50	36.8	99.4	85.7	88.3

Table 3 shows the optimized coefficients of $f(v)$ for different value of N .

The nonlinear element of $f(v)$ could be approximated by linear partial function which is shown in Fig. 11. This graph function was simply implemented in CMOS technology [16-18].

6 Conclusions

This paper present a new structure for fast lock PLL based on embedding nonlinear element. In this method the Genetic Algorithm was used to find the best coefficients of nonlinear element. As it's seen, the optimal nonlinear element increases the speed of PLL and simultaneously decreases overshoot. According to the results, using Genetic Algorithm for estimate the coefficients of nonlinear improve phase step response considerably.

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Mariam Zabihi received the B.Sc. degree in 2006 and the M.Sc. degree in 2009 in electronics engineering from Babol University of Technology, Babol, Iran. Her research interests are Signal Processing, Perturbation Methods, CMOS Analog Design.



Hossein Miar-Naimi was born in Chalous, Iran, in 1972. received the B.Sc. from Sharif University of Technology in 1994 and M.Sc. from Tarbiat Modares University in 1996 and Ph.D. from Iran University of Science and Technology in 2002 all in Electrical Engineering respectively. Since 2003 He has been member of Electrical and Electronics Engineering Faculty of Babol University of Technology. His research interests are analog CMOS integrated circuit design, RF & Microwave microelectronics.