

Iranian Journal of Electrical and Electronic Engineering

Journal Homepage: ijeee.iust.ac.ir

# Harmonics Elimination of Reduced Switch Multilevel Inverter Using Henry Gas Solubility Optimization Algorithm

Murni Nabila Mohd Zawawi\*, Zainuddin Mat Isa \*\*<sup>(C.A.)</sup> Baharuddin Ismail<sup>\*\*</sup>, Mohd Hafiz Arshad<sup>\*</sup>, Ernie Che Mid<sup>\*\*(C.A.)</sup>, Md Hairul Nizam Talib<sup>\*\*\*</sup>, and Muhammad Fitra Zambak \*\*\*\*

Abstract: This study introduces a pioneering method to enhance the efficiency and effectiveness of three-phase five-level reduced switch cascaded H-bridge multilevel inverters (CHB MLI) by employing the Henry Gas Solubility Optimization (HGSO) algorithm. Targeting the selective harmonic elimination (SHE) technique, the research emphasizes the optimization of switching angles to significantly reduce total harmonic distortion (THD) and align the fundamental output voltage closely with the reference voltage. Central to this exploration are three distinct objective functions (OFs), meticulously designed to assess the HGSO algorithm's performance across various modulation indices. Simulation results, facilitated by PSIM software, illustrate the impactful role these objective functions play in the optimization process. OF1 demonstrated a superior ability in generating low OF values and maintaining a consistent match between reference and fundamental voltages across the modulation index spectrum. Regarding the reduction of THD, it is crucial to emphasize that all OFs can identify the most effective switching angle to minimize THD and eliminate the fifth harmonic to a level below 0.1%. The findings highlight the potential of HGSO in solving complex optimization challenges within power electronics, offering a novel pathway for advancing modulation strategies in CHB MLIs and contributing to the development of more efficient, reliable, and compact power conversion systems.

**Keywords:** Henry Gas Solubility Optimization Algorithm, Multilevel Inverter, Reduced Switch, Selected Harmonic Elimination.

# 1 Introduction

**P** OWER electronics has focused on multilevel inverters (MLIs) since they connect renewable

energy sources to the grid [1], [2], [3]. The Cascaded H-Bridge (CHB) architecture is ideal for high-power applications due to its versatility and scalability [4], [5]. Recent CHB MLI trends emphasize efficiency, THD reduction, and power quality. CHBs are easy to implement and can run with different DC sources for each H-bridge, improving reliability and fault tolerance, but they also bring problems. The complexity of control strategies needed to balance DC connection voltages, and the increased number of power electronic components can increase expenses and maintenance [6], [7], [8].

To get the desired performance, CHB MLI modulation methods must be carefully chosen [9]. Selective Harmonic Elimination (SHE) is unique in its ability to correctly alter the output waveform and minimize THD [10], [11], [12], [13]. SHE methods determine the best

Iranian Journal of Electrical & Electronic Engineering, 2025. Paper first received 23 Dec. 2024 and accepted 20 Feb. 2025. \* The authors are with the Department of Electrical Engineering, Faculty of Electrical Engineering & Technology, Universiti Malaysia Perlis, 02600 Arau, Perlis, Malaysia \*\*\* The authors are with the Centre of Excellence for Renewable Energy, Universiti Malaysia Perlis, 02600 Arau, Perlis, Malaysia. E-mails: <u>zainuddin@unimap.edu.my</u>, <u>ernie@unimap.edu.my</u> \*\*\* The author is with the Department Electrical Engineering, Faculty of Electrical Engineering, Universiti Teknikal Malaysia Melaka, 76100, Melaka, Malaysia

<sup>\*\*\*\*</sup> The author is with the Department of Electrical Engineering, Universitas Muhammadiyah Sumatera Utara, 20238 Medan, Indonesia

Corresponding Author: Zainuddin Mat Isa, Ernie Che Mid

switching angles to reduce harmonics while keeping the fundamental frequency. Due to its complexity and constant change, resolving non-linear transcendental equations is difficult when using this method. Iterative numerical methods, algebraic resolves, and bio-inspired algorithms are all used to handle these difficulties, each with pros and downsides. Although iterative numerical methods like the Newton-Raphson technique are accurate, they can be resource-intensive [14], [15]. Bio-inspired algorithms like the Genetic Algorithm (GA) [16], Particle Swarm Optimization (PSO) [17], Bee Algorithm (BA) [18], and Whale Optimization Algorithm (WOA) [19] balance precision with processing speed to meet SHE's evolving needs.

The Henry Gas Solubility Optimization (HGSO) algorithm is an innovative addition to the optimization field, drawing inspiration from Henry's Law, a physical theory that explains the solubility of gases in liquids [20]. This method emulates the inherent process of gas dissolution to address intricate optimization problems, providing a novel approach to discovering global optima in multi-dimensional domains. HGSO stands out by skillfully managing the exploration and exploitation stages, therefore preventing early fixation on suboptimal solutions, and showcasing exceptional performance in benchmark optimization tasks [20], [21]. Its application goes beyond conventional optimization problems, demonstrating potential in optimizing modulation strategies for MLIs. It can aid in the efficient calculation of switching angles for SHE methods, thereby improving the overall performance and efficiency of cascaded Hbridge multilevel inverters [22].

Based on the previously mentioned analysis of recent literature, the topologies highlighted possess different points of view concerning their structure, modularity, and overall power components. However, for three phase topology configurations, conventional topologies demand a high number of power components. Therefore, this provides an opportunity for the development of a new modular topology with reduced power components and DC sources. This study proposes a new three-phase five-level CHB MLI design with a reduced number of switches and expected to be a compact structure. The SHE technique will be applied with the help of HGSO as an optimization-based technique to obtain the optimized switching angles. Its benefits have been explored through an analysis of three different objective functions. A simulation has been created by using PSIM software to assess the feasibility of the proposed topology.

# 2 Methodology

# 2.1 Reduced Switch Cascaded H-Bridge Multilevel Inverter

Due to their natural ability to generate stepped voltage waveforms to other output voltage waveforms of higher quality, MLI topologies have been the focus of many research studies in the development of topological structures. From this perspective, an effort has been carried out to create a new, three-phase five-level CHB MLI with reduced switch for the same amount of DC sources using a single-phase structure [23]. Fig. 1 shows a generalized structure for three-phase topology made up of five-level (0V<sub>DC</sub>, 1V<sub>DC</sub>, 2V<sub>DC</sub>, -1V<sub>DC</sub>, -2V<sub>DC</sub>) voltage generation modules connected in series with switching devices. The switching scheme for proposed topology is tabulated in Table 2. Referring to the table, there will be five stages of output voltage generated from  $0 V_{\text{DC}}$  to - $2V_{DC}$ . To clearly explain the operating system, Fig. 2 depicts a generalized single-phase structure to generate stepped voltage waveform as shown in Fig. 3.



Fig. 1 Generalized structure for three-phase five-level CHB MLI.

 Table 1 Switching scheme for proposed five-level CHB MLI topology.

Output Voltage	Switches						
	<b>S1</b>	<b>S2</b>	<b>S</b> 3	<b>S</b> 4	S5	S6	
2V <sub>DC</sub>	1	1	1	0	0	1	
1V <sub>DC</sub>	1	0	1	0	0	1	
0V <sub>DC</sub>	0	0	0	1	0	1	
-1V <sub>DC</sub>	1	0	0	1	1	0	
-2V <sub>DC</sub>	1	1	0	1	1	0	



Fig. 2 The operation of five-level CHB MLI proposed topology: (a) 0VDC (b) VDC (c) 2VDC (d) -VDC (e) -2VDC

To produce 0V<sub>DC</sub> of output voltage, the switches S4 and S6 will be turned on and short-circuiting the load. In this stage, there will be no supply sent to the load as shown in Fig. 2(a). On a positive half-cycle basis, the switches S3 and S6 from the H-bridge side will be turned on. To produce the output voltage of Vdc, the DC source VDC1 supplied to the load through switch S1 and the diode D2. The operation is shown in Fig. 2(b). Next, both the DC sources of VDC1 and VDC2 are supplied to the load through switch S1 and S2 to produce the output voltage of 2V<sub>DC</sub>. The switches S3 and S6 from the Hbridge side have remained turned on as shown in Fig. 2(c). On a negative half-cycle basis, the switches S4 and S5 will be turned on instead of the switches S3 and S6 to generate -V<sub>DC</sub> output voltage. Same as previous in the second stage, the load will be supplied by the DC source VDC1 through switch S1 and the diode D2 as shown in Fig. 3(d). Lastly,  $-2V_{DC}$  of output voltage is generated when the load is supplied by the DC sources VDC1 and VDC2 through the switches S1 and S2. From the Hbridge side, the switches S4 and S5 remained turned on as shown in Fig. 2(e).

#### 2.2 Selective Harmonic Elimination Switching Scheme

The SHE technique entails determining the most favorable switching angles to eliminate particular lowerorder harmonics while preserving the fundamental frequency component of the voltage waveform. The process involves solving a collection of nonlinear transcendental equations that accurately reflect the harmonic components of the inverter's output voltage. Before continuing with solving the SHE problems, the staged output waveform can be expressed in Fourier Series expression as in Eq. (1).

$$V(\omega t) = a_0 + \sum_{n=1}^{\infty} a_n \cos(n\omega t) + b_n \sin(n\omega t)$$
(1)

where,  $a_0$  is DC component,  $a_n$  is even harmonics, and  $b_n$  is odd harmonics.



In order to eliminate unwanted low order harmonics, the optimum switching angles are required. Since the output voltage waveform produced is in stepped wave with the property of quarter wave symmetry (QWS), the DC component, even harmonics and odd harmonics for cosine term are equals to zero. Thus, Eq. (1) can be rewritten as Eq. (2).

$$V(\omega t) = \sum_{n=1}^{\infty} b_n \sin(n\omega t) = \sum_{n=1}^{\infty} V_n \sin(n\omega t)$$
(2)

where  $V_n$  is notated as magnitude of  $n^{th}$  order harmonic. According to the odd symmetry of QWS, even order harmonics are zero, then  $V_n$  is expressed as in Eq. (3).

$$V_n = \begin{cases} \frac{4V_{DC}}{n\pi} \sum_{i=1}^n \cos(na_i); n = odd \\ 0; n = even \end{cases}$$
(3)

In order to lessen the harmonic distortion of the fundamental peak voltage,  $V_1$ , the switching angles must be within  $0 \le a_1 \le a_2 \dots \le a_n \le \pi/2$ . The number of harmonics that can be eliminated in a single-phase five-level CHB MLI is  $n = \frac{L-1}{2}$ , where L is the number of the level. Therefore, there will be three non-linear expressions since the total number of harmonics that may be eliminated is n - 1 (n = 3). For three-phase, triple-n order harmonics (third, sixth, nineth...) will be eliminated. Thus, general Eq. (2) for the first and fifth harmonic orders are given as in Eq. (4).

$$\frac{4V_{DC}}{\pi}(\cos(a_1) + \cos(a_2)) = V_1$$

$$\frac{4V_{DC}}{5\pi}(\cos(5a_1) + \cos(5a_2)) = V_5$$
(4)

The modulation index,  $M_i$  which is the ratio of referred fundamental voltage and supply voltage is given in Eq. (5). Arranging the equation, the  $V_{ref}$  can be expressed as Eq. (6).

$$M_i = \frac{\pi V_{ref}}{4nV_{DC}} \tag{5}$$

$$V_{ref} = \frac{4nV_{DC}M_i}{\pi} \tag{6}$$

Basically, to optimize the switching angles, the  $V_1$  is set to equal to  $V_{ref}$  and any undesired individual harmonic is set to 0. Considering this the expression in Eq. (4) can be rewritten as in Eq. (7).

$$\frac{4V_{DC}}{\pi}(\cos(a_1) + \cos(a_2)) = \frac{4nV_{DC}M_i}{\pi}$$

$$\frac{4V_{DC}}{5\pi}(\cos(5a_1) + \cos(5a_2)) = 0$$
(7)

#### 2.3 Henry Gas Solubility Optimization

A population-based metaheuristic algorithm that was just introduced, the Henry Gas Solubility Optimization Algorithm (HGSO) stands out for breaking the population up into N clusters, each of which is mapped to a separate HGSO with its own set of parameters and local best. Although this clustering method improves exploration, there are two key drawbacks noted [25]. Firstly, there is a lack of flexibility in the static mapping of HGSO to clusters because it does not alter over time. Secondly, the algorithm fails to consider the possible advantages of hybridization, an approach that enhances outcomes by merging the advantages of many algorithms [25], [26]. Figure 4 depicts the pseudocode for HGSO algorithm.

1	begin
2	Initialize population $X_g(g = 1.2,, N)$ , number
	of gas types and all parameters
	$(g.H_i.P_{g.i}.C_i.l_1.l_2.and l_3,)$
3	Divide the population agents into a number of gas
	types (cluster) with the same $H_i$ .
4	Evaluate each cluster <i>i</i>
5	Get the best gas $X_{g.best}$ and the best search agent,
	X <sub>best</sub>
6	while $t < Iteration_{max}$
7	For each search agent do
8	Update the search agent individual
	position
9	end for
10	Update Henry's coefficient of each gas
11	Update solubility for each gas
12	Rank and select the number of worst agents
13	Update the position of the worst agents
14	Update the best gas $X_{g.best}$ and the best search
	agent, X <sub>best</sub>
15	t = t + 1
16	end while
17	return X <sub>best</sub>
18	end

Fig. 4 HGSO pseudocode [27].

#### 2.4 Formulation of the Optimization Problem

Before using an optimization procedure to solve the SHE problem, the objective function (OF) needs to be defined with the following goals in mind:

- 1. Eliminate the difference between reference voltage  $(V_{ref})$  and fundamental voltage  $(V_1)$ . The  $V_{ref}$  is targeted fundamental voltage depends on specific modulation index  $(M_i)$ .
- 2. Eliminate the desired harmonics.

In this paper, two main switching angles need to be precisely optimized, and the fifth order harmonic is required to be eliminated. Thus, the generic OF can be written as in Eq. (8)[28], [29]:

$$\begin{aligned} \text{Minimize, } f_{fit} & (\alpha_1, \alpha_2) \\ = \left( \varepsilon_1 \frac{V_{ref} - V_1}{V_{ref}} \right)^x + \frac{1}{5} \left( \varepsilon_2 \frac{V_5}{V_1} \right)^y \end{aligned} \tag{8}$$

In Eq. (8), the first term represents the fundamental voltage difference between the reference and derived values and is referred to as the basic component. Additionally, the second term refers to the components used for eliminating harmonics. The  $\varepsilon_1$  and  $\varepsilon_2$  are the weight penalty factors associated with each phrase, respectively. This value is often chosen within the range of 1 to 100. The higher the value, the more it has the substantial effect on the overall performance. For instance, if this number is set to 100, when the value varied by 1%, the fitness function contributed by this term is increased by the factor of 100. Meanwhile, the xand y values represent the power penalty factor, which is used to apply a greater penalty if the calculated value exceeds the specified range of significance. Typically, terms with greater significance will incur a higher penalty amount. Three distinct objective functions are established based on the data presented in Table 2. The aim is to analyse the influence of various factors on the performance of HGSO in attaining the optimal switching angle using the SHE approaches.

Table 2 Objective functions parameters.

Objective Function	Switches					
	ε <sub>1</sub>	ε2	x	у	Notes	
OF1	100	50	4	2	Term 1 is set to be more important	
OF2	50	100	2	4	Term 2 is set to be more important	
OF3	100	100	2	2	Both is set to equally important	

#### **3** Results And Discussion

The optimal switching angles are determined by utilizing three OFs in the HGSO algorithm. The values are determined through the application of MATLAB program. The quantity of gases is established at 50, divided into five groups. The maximum number of iterations is set to 1000. Other settings are based on HGSO original parameters given in [27]. The modulation index, ranging from 0 to 1, is utilized to compute the switching angles using a step size of 0.01. The procedure is iterated 10 times for each modulation index value. Fig. 5 displays the cumulative distribution function (CDF) for all objective functions. Cumulative distribution function (CDF) is primarily employed to assess the ability of the HGSO algorithm to generate OF values that are deemed satisfactory. A higher proportion corresponds to a superior level of ability. Upon examining this figure, within the range of permitted values for objective functions, which is  $10^2$  [30], [31], the acceptable percentages for OF1, OF2, and OF3 are 53.6%, 51.6%, and 45.3% correspondingly. This first evidence the superiority of OF1 compared to other OFs.



Fig. 5 Cumulative distribution function (CDF).

The modulation index is divided into three stages: the lower stage, ranging from 0 to 0.30; the middle stage, ranging from 0.31 to 0.70; and the higher stage, ranging from 0.71 to 1.00. Fig. 6 depicts the relationship between the objective function value and the modulation index. Observing this figure, during the medium stage, the minimum values for OF1, OF2, and OF3 occurred at a modulation index of 0.44 (OF =  $8.35E^{-9}$ ), 0.56 (OF =  $1.94E^{-8}$ ), and 0.39 (OF =  $2.51E^{-6}$ ), respectively. At a higher stage, the minimum values for OF1, OF2, and OF3 are  $1.61E^{-10}$  ( $M_i = 0.71$ ),  $2.62E^{-9}$ ( $M_i = 0.80$ ), and  $3.79E^{-5}$  ( $M_i = 0.87$ ), correspondingly.



Fig. 6 Objective function value versus modulation index.

This study aims to observe how different factors affect the performance of HGSO in achieving the optimal switching angle utilizing the SHE techniques. The first observation is on the difference between reference and fundamental voltage. Fig. 7 displays the proportion of fundamental voltage achieved at each modulation index for the OF. Upon examination of the figure, it is evident that the fundamental voltage,  $V_1$ , obtained from OF1 is consistently aligned with  $V_{ref}$  across all modulation index stages. The percentage for  $V_1$ , derived from OF2 and OF3, shows minimal discrepancy up to a modulation index of 0.4, and subsequently aligns with  $V_{ref}$ .



Fig. 7 Fundamental and reference voltages versus modulation index.

The second observation relates to THD. Fig. 8 illustrates the THD performance as a function of the modulation index for OF1, OF2, and OF3. The modulation index of 0.61 (middle stage index) results in the lowest THD values of 16.45%, 16.44%, and 16.43% correspondingly. The lowest THD values for OF1, OF2, and OF3 are 0.91, with values of 11.02%, 11.01%, and 11.01% respectively, within a higher range of modulation index. OF3 has the lowest THD in both the medium and upper stages of the index, in comparison to the THD values derived from OF1 and OF2.



Fig. 8 THD versus modulation index.

To facilitate additional examination, the switching angles associated with the minimum THD generated across all modulation indices have been chosen and organized in Table 3. The suggested three-phase, fivelevel CHB MLI utilizes these switching angles to observe the harmonic contents. Fig. 9 presents a graphical representation of the harmonics associated with each of the chosen modulation index values. In a three-phase system, it is widely acknowledged that triplen harmonics are non-existent. Hence, the harmonic that is specifically targeted for elimination in this investigation is solely the fifth harmonic. Table 3 presents the values of fifth harmonics alongside the THD value for each chosen modulation index.

 Table 3 The optimal switching angle value for OF1, OF2, and OF3.

Objective Function	M <sub>i</sub>	α <sub>1</sub>	α2	5 <sup>th</sup>	THD (%)		
				Harmonics	Simulation	Calculation	
OF1	0.61	41.80	77.82	1.80E <sup>-04</sup>	18.08	16.45	
	0.91	23.24	59.24	4.28E <sup>-05</sup>	12.42	11.02	
OF2	0.61	41.81	77.73	6.88E <sup>-04</sup>	18.03	16.43	
	0.91	23.27	59.28	2.84E <sup>-04</sup>	12.41	11.01	
OF3	0.61	41.83	77.78	$4.04E^{-04}$	18.05	16.43	
	0.91	23.25	59.31	9.91E <sup>-04</sup>	12.40	11.00	

Upon examination of each individual harmonic depicted in Fig. 9, it is observed that the fifth harmonic exhibits a substantial reduction, amounting to less than 0.1%. The angels generated from OF1, OF2, and OF3 exhibited the most significant reduction with a modulation index of 0.91. In the context of modulation index 0.61, it was observed that the application of angles OF2 resulted in the lowest value of the fifth harmonic, followed by OF3 and OF2. The angles achieved through optimization using various objective functions exhibit the capability to achieve the lowest THD and eliminate the desired harmonics (with a simulation value below 0.1%).

#### 4 Conclusions

This paper presented an innovative approach to optimizing a three-phase five-level reduced switch CHB MLI using the HGSO algorithm, with a focus on the SHE technique. The study showcased the effectiveness of the HGSO algorithm in minimizing THD and aligning the fundamental output voltage with the reference voltage across different modulation indices, achieved through careful execution of three separate OFs. The findings of the study revealed that OF1 had superior







(b) OF1 (Mi=0.91)





(d) OF2 (Mi=0.91)



(e) OF3 (Mi=0.61)



(f) OF3 (Mi=0.91)

Fig. 9 Individual harmonic graph and THD value for selected Modulation Index (a) OF1 ( $M_i = 0.61$ ) (b) OF1 ( $M_i = 0.91$ ) (c) OF2 ( $M_i = 0.61$ ) (d) OF2 ( $M_i = 0.91$ ) (e) OF3 ( $M_i = 0.61$ ) (f) OF3 ( $M_i = 0.91$ )

performance compared to other OFs in terms of attaining lower OF values and sustaining a consistently close alignment between reference and fundamental voltages across the entire modulation index spectrum. In the context of minimizing THD, it is important to highlight that all OFs have the capability to determine the optimal switching angle that may effectively reduce THD and eliminate the fifth harmonic to a level below 0.1%.

# **Conflict of Interest**

The authors declare no conflict of interest.

# **Author Contributions**

The experiment and simulation were designed by Murni Nabila and Zainuddin. The optimization algorithm was simulated by Mohd Hafiz, Baharuddin, and M. Fitra. The analysis of results was conducted by Zainuddin, Ernie, and Md Hairul Nizam. All authors participated in data interpretation, as well as in the writing and revision of the article.

## Funding

This project has received funding under the Fundamental Research Grant Scheme (FRGS) under a grant number of FRGS/1/2020/TK0/UNIMAP/03/17 from the Ministry of Higher Education Malaysia.

## Acknowledgment

The author would like to acknowledge the support from the Fundamental Research Grant Scheme (FRGS) under a grant number of FRGS/1/2020/TK0/UNIMAP/03/17 from the Ministry of Higher Education Malaysia.

## References

- [1] N. Prabaharan and Κ. Palanisamy, "A comprehensive review on reduced switch multilevel inverter topologies, modulation techniques and applications," Renewable and Sustainable Energy Reviews, vol. 76. 2017. doi: 10.1016/j.rser.2017.03.121.
- [2] S. Choudhury, M. Bajaj, T. Dash, S. Kamel, and F. Jurado, "Multilevel inverter: A survey on classical and advanced topologies, control schemes, applications to power system and future prospects," *Energies (Basel)*, vol. 14, no. 18, 2021, doi: 10.3390/en14185773.
- [3] C. S. Kumar and S. T. Kalyani, "Improvement of Power Quality in Smart Grid Connected P.V. System Using Multi-Level Inverter," *Journal of Advanced Research in Applied Sciences and Engineering Technology*, vol. 31, no. 1, pp. 1–13, Jun. 2023, doi: 10.37934/araset.31.1.113.
- [4] H. S. Maaroof, H. Al-Badrani, and A. T. Younis, "Design and simulation of cascaded H-bridge 5-

level inverter for grid connection system based on multi-carrier PWM technique," *IOP Conf Ser Mater Sci Eng*, vol. 1152, no. 1, p. 012034, May 2021, doi: 10.1088/1757-899x/1152/1/012034.

- [5] M. Malinowski, K. Gopakumar, J. Rodriguez, and M. A. Perez, "A survey on cascaded multilevel inverters," *IEEE Transactions on Industrial Electronics*, vol. 57, no. 7. 2010. doi: 10.1109/TIE.2009.2030767.
- [6] M. Barghi Latran and A. Teke, "Investigation of multilevel multifunctional grid connected inverter topologies and control strategies used in photovoltaic systems," *Renewable and Sustainable Energy Reviews*, vol. 42. 2015. doi: 10.1016/j.rser.2014.10.030.
- [7] R. Castillo, B. Diong, and P. Biggers, "Singlephase hybrid cascaded H-bridge and diodeclamped multilevel inverter with capacitor voltage balancing," *IET Power Electronics*, vol. 11, no. 4, pp. 700–707, Apr. 2018, doi: 10.1049/ietpel.2017.0009.
- [8] T. Qanbari and B. Tousi, "Single-Source Three-Phase Multilevel Inverter Assembled by Three-Phase Two-Level Inverter and Two Single-Phase Cascaded H-Bridge Inverters," *IEEE Trans Power Electron*, vol. 36, no. 5, pp. 5204–5212, May 2021, doi: 10.1109/TPEL.2020.3029870.
- [9] R. Vijayakumar, "Selective Harmonic Elimination PWM Method using Seven Level Inverters by Genetic Algorithm Optimization Technique," *International Journal of Engineering Research & Technology (IJERT)*, vol. 4, no. 02, 2015.
- [10] G. K. Devineni and A. Ganesh, "Problem formulations, solving strategies, implementation methods & applications of selective harmonic elimination for multilevel converters," *Journal Europeen des Systemes Automatises*, vol. 53, no. 6, 2020, doi: 10.18280/jesa.530620.
- [11] T. Daniel Raj, M. A. Mulla, C. S. Patil, and G. A. Chougule, "A new method of selective harmonic elimination PWM for H-bridge cascaded multilevel inverter," *Int J Eng Adv Technol*, vol. 9, no. 1, 2019, doi: 10.35940/ijeat.A1460.109119.
- [12] M. Srndovic, A. Zhetessov, T. Alizadeh, Y. L. Familiant, G. Grandi, and A. Ruderman, "Simultaneous Selective Harmonic Elimination and THD Minimization for a Single-Phase Multilevel Inverter with Staircase Modulation," *IEEE Trans Ind Appl*, vol. 54, no. 2, 2018, doi: 10.1109/TIA.2017.2775178.
- [13] M. Saifizi, M. I. Fahmi, M. Othman, Wan Azani Mustafa, M. Z. Aihsan, M. R. Manan, and Azri A. Aziz., "Implementation of Two-Stage Multilevel Inverter System Using PIC Controller," *Journal of*

Advanced Research in Applied Sciences and Engineering Technology, vol. 28, no. 2, pp. 41–55, Oct. 2022, doi: 10.37934/araset.28.2.4155.

- [14] R. Shahbaz, T. Ahmed, R. M. Elavarasan, K. Raju, M. Waqas, and U. Subramaniam, "Selective Harmonics Elimination in Multilevel Inverter Using Bio-Inspired Intelligent Algorithms," in Proceedings of 2021 31st Australasian Universities Power Engineering Conference, AUPEC 2021, 2021. doi: 10.1109/AUPEC52110.2021.9597805.
- [15] M. A. Memon, S. Mekhilef, M. Mubin, and M. Aamir, "Selective harmonic elimination in inverters using bio-inspired intelligent algorithms for renewable energy conversion applications: A review," *Renewable and Sustainable Energy Reviews*, vol. 82. 2018. doi: 10.1016/j.rser.2017.08.068.
- [16] S. Urgun and H. Yigit, "Selective harmonic eliminated pulse width modulation (SHE-PWM) method using genetic algorithm in single-phase multilevel inverters," *International Journal on Electrical Engineering and Informatics*, vol. 13, no. 1, 2021, doi: 10.15676/IJEEI.2021.13.1.11.
- [17] M. T. Yaqoob, Z. Shahid, M. K. Rahmat, M. M. Alam, and M. M. Su'ud, "Selective Harmonic Elimination in Cascaded H-Bridge Multilevel Inverters using Particle Swarm Optimization: A review," in MACS 2019 - 13th International Conference on Mathematics, Actuarial Science, Computer Science and Statistics, Proceedings, 2019. doi: 10.1109/MACS48846.2019.9024783.
- [18] A. Kavousi, B. Vahidi, R. Salehi, M. K. Bakhshizadeh, N. Farokhnia, and S. H. Fathi, "Application of the bee algorithm for selective harmonic elimination strategy in multilevel inverters," *IEEE Trans Power Electron*, vol. 27, no. 4, pp. 1689–1696, 2012, doi: 10.1109/TPEL.2011.2166124.
- [19] A. K. Koshti and M. N. Rao, "A brief review on multilevel inverter topologies," in 2017 *International Conference on Data Management, Analytics and Innovation*, ICDMAI 2017, 2017. doi: 10.1109/ICDMAI.2017.8073508.
- [20] F. A. Hashim, E. H. Houssein, M. S. Mabrouk, W. Al-Atabany, and S. Mirjalili, "Henry gas solubility optimization: A novel physics-based algorithm," *Future Generation Computer Systems*, vol. 101, 2019, doi: 10.1016/j.future.2019.07.015.
- [21] F. A. Hashim, E. H. Houssein, K. Hussain, M. S. Mabrouk, and W. Al-Atabany, "A modified Henry gas solubility optimization for solving motif discovery problem," *Neural Comput Appl*, vol. 32, no. 14, pp. 10759–10771, 2020, doi: 10.1007/s00521-019-04611-0.

- [22] N. Neggaz, E. H. Houssein, and K. Hussain, "An efficient henry gas solubility optimization for feature selection," *Expert Syst Appl*, vol. 152, 2020, doi: 10.1016/j.eswa.2020.113364.
- [23] C. S. Kumar and S. T. Kalyani, "Improvement of Power Quality Using Reduced Switch Count Eleven-Level Inverter in Smart Grid," *Journal of Advanced Research in Applied Sciences and Engineering Technology*, vol. 31, no. 1, pp. 14–33, Jun. 2023, doi: 10.37934/araset.31.1.1433.
- [24] M. N. M. Zawawi, Z. M. Isa, M. H. Arshad, B. Ismail, and M. H. N. Talib, "Comparative Study of Multiphase 5-Level Cascaded H-Bridge Multilevel Inverter System," in *Journal of Physics: Conference Series*, Institute of Physics, 2022. doi: 10.1088/1742-6596/2312/1/012062.
- [25] K. Z. Zamli, A. Kader, S. Azad, and B. S. Ahmed, "Hybrid Henry Gas Solubility Optimization Algorithm with Dynamic Cluster-to-Algorithm Mapping for Search-based Software Engineering Problems," *Neural Comput Appl*, 2021, doi: https://doi.org/10.48550/arXiv.2105.14923.
- [26] D. M. Utama, B. N. I. Farida, U. Fitriani, M. F. Ibrahim, and D. S. Widodo, "Hybrid Henry Gas Solubility Optimization: An Effective Algorithm for Fuel Consumption Vehicle Routing Problem," *Jurnal Ilmiah Teknik Industri*, vol. 20, no. 2, pp. 141–152, Dec. 2021, doi: 10.23917/jiti.v20i2.15640.
- [27] F. A. Hashim, E. H. Houssein, M. S. Mabrouk, W. Al-Atabany, and S. Mirjalili, "Henry gas solubility optimization: A novel physics-based algorithm," *Future Generation Computer Systems*, vol. 101, pp. 646–667, Dec. 2019, doi: 10.1016/j.future.2019.07.015.
- [28] A. Niknam Kumle, S. H. Fathi, F. Jabbarvaziri, M. Jamshidi, and S. S. Heidari Yazdi, "Application of memetic algorithm for selective harmonic elimination in multi-level inverters," *IET Power Electronics*, vol. 8, no. 9, pp. 1733–1739, Sep. 2015, doi: 10.1049/iet-pel.2014.0209.
- [29] M. Salman et al., "Minimization of total harmonic distortions of cascaded H-bridge multilevel inverter by utilizing bio inspired AI algorithm," *EURASIP J Wirel Commun Netw*, vol. 2020, no. 1, Dec. 2020, doi: 10.1186/s13638-020-01686-5.
- [30] Y. Xin, J. Yi, K. Zhang, C. Chen, and J. Xiong, "Offline Selective Harmonic Elimination with (2N+1) Output Voltage Levels in Modular Multilevel Converter Using a Differential Harmony Search Algorithm," *IEEE Access*, vol. 8, pp. 121596–121610, 2020, doi: 10.1109/ACCESS.2020.3007022.
- [31] A. K. Kaviani, S. H. Fathi, N. Farokhnia, and A. J.

Ardakani, "PSO, an effective tool for harmonics elimination and optimization in multi-level inverters," 2009 4th *IEEE Conference on Industrial Electronics and Applications*, ICIEA 2009, no. June, pp. 2902–2907, 2009, doi: 10.1109/ICIEA.2009.5138740.

#### **Biographies**



Murni Nabila Binti Mohd Zawawi 🕩 🐰 💈 is a postgraduate researcher at Faculty of Electrical Engineering & Technology, Universiti Malaysia Perlis, Malaysia since 2021, after earning her B.Eng. in Electrical Engineering. Before taking on her current employment, she held the position of junior project

engineer at Oversea Lighting and Electric (M) Sdn. Bhd., located in Kampung Baru Balakong from 2019 to 2020. Her research pursuits centers around power electronics, industrial electronics and application, motor drives, and harmonic elimination



Zainuddin Mat Isa 厄 🔣 🚾 🕐 is a Senior Lecturer at Universiti Malaysia Perlis, specializing in Electrical Engineering. He holds a Master of Science (MSc) degree in Electrical Engineering from Universiti Teknikal Malaysia Melaka (2006) and a Bachelor of Electrical Engineering degree from Universiti Teknologi Malaysia

(2001). With over 15 years of teaching experience, he has been actively involved in shaping the minds of aspiring engineers. His research interests encompass Power Electronics, Optimization, and Renewable Energy. He's dedication to his field and passion for sustainable solutions drives his contributions to academia and the development of efficient and clean power generation methods.



Baharuddin Bin Ismail D K received the B.Eng. degree in Electrical Engineering from Universiti Teknologi Malaysia in 2000, M.Sc. degree in Electrical and Electronic Engineering from Universiti Sains Malaysia in 2008 and Ph.D. degree in Electrical System Engineering from Universiti Malaysia Perlis in 2016. He is currently

Associate Professor with the Centre of Excellence for Renewable Energy (CERE), Faculty of Electrical Engineering & Technology, Universiti Malaysia Perlis, Malaysia. His current research interests include Renewable Energy, Multilevel Inverter, Power quality and Energy Efficiency. He has an extensive portfolio of relevant research supported by a variety of funding agencies for local and international. He actively participates in international conferences and workshops. He published more than 100 papers in international conferences and journals. In addition, he acts as a consultant to several industrial companies that involve in Renewable energy, Power quality and Energy efficiency.



Mohd Hafiz Arshad, 🔟 🔀 🗺 presently а Lecturer at University Malaysia Perlis (UniMAP), has forged a distinguished path in the field of electrical engineering. His academic journey began at UniMAP, where he attained a bachelor's degree in electrical engineering in 2008. Building on this foundation, he furthered

his education at the same institution, completing a master's degree in electrical engineering in 2013. His role as a lecturer, held since 2013, reflects his commitment to both educating and researching in the field. Mohd Hafiz Arshad's research interests encompass Multilevel Inverters, Power Electronics, and Renewable Energy, critical areas that drive advances in electrical engineering. Additionally, his active engagement in innovation underscores his dedication to pushing the boundaries of knowledge and technology within the field. As he continues his career, Mohd Hafiz Arshad remains a source of inspiration and innovation within the academic and engineering communities.



2 SC Ernie Che Mid 问 received the BEng and MEngSc. degrees in electrical engineering from Universiti Malaya, in 2006 and 2009, respectively. She obtained the Ph.D degree in University of College London, 2019. She is currently working as senior lecturer at Faculty of Electrical Engineering & Technology,

Universiti Malaysia Perlis, Malaysia. Her current research interests include fault detection, parameter estimation and control and optimization algorithm.



Md Hairul Nizam Talib D K s is an Associate Professor at Universiti Teknikal Malaysia Melaka (UTeM). He received his B.S. in Electrical Engineering from the Universiti Teknologi Malaysia (UTM), Johor, Malaysia, in 1999, M.S. in Electrical Engineering from the University of Nottingham, Nottingham, UK, in 2005 and

PhD from the Malaysia in 2016. His main research

interests include power electronics, fuzzy logic control and electrical motor drives.



Muhammad Fitra Zambak Was born in the city of Sibolga in 1987. The author is Muslim and is married. The author completed elementary school at SDN 081236 Aek Habil Village, Sibolga City, from 1993 to 1999. Then continued studies at Junior High School SMPN3 Sibolga City

from 1999-2002. Next, the author continued to SMK-TI N1 Sibolga Tapanuli Tengah, majoring in Electrical Engineering from 2002 to 2005. Next, the author continued their studies at the university level. The author completed their undergraduate studies at the Medan Institute of Technology, majoring in Electrical Engineering from 2005 to 2009, thus obtaining a Bachelor of Engineering (B.Eng) degree. Next, the author pursued a Master's degree at Universiti Malaysia Perlis (UniMAP), Malaysia from 2011-2013 and successfully obtained an M.Sc. degree. Finally, the author completed their Ph.D. at Universiti Malaysia Perlis (UniMAP), Malaysia from 2014 to 2016.